



Silicon N-Channel Power MOSFET



CS150N04 A8

## General Description:

CS150N04 A8, the silicon N-channel Enhanced VDMOSFETs, is obtained by advanced trench Technology which reduce the conduction loss, improve switching performance and enhance the avalanche energy. The transistor can be used in various power switching circuit for system miniaturization and higher efficiency. The package form is TO-220AB, which accords with the RoHS standard.

## Features:

- | **Fast Switching**
- | **Low ON Resistance( $R_{DS(on)} \leq 4.5\text{m}\Omega$ Typ4 $\text{m}\Omega$ )**
- | **High Power and Current Handling Capability**
- | **Low Reverse transfer Capacitances(Typical:480pF)**
- | **100% Single Pulse avalanche energy Test**

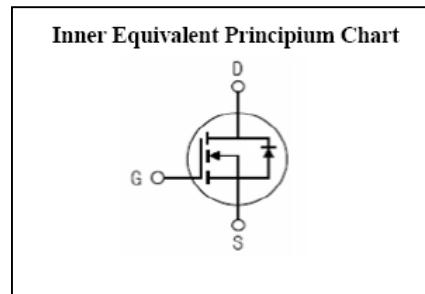
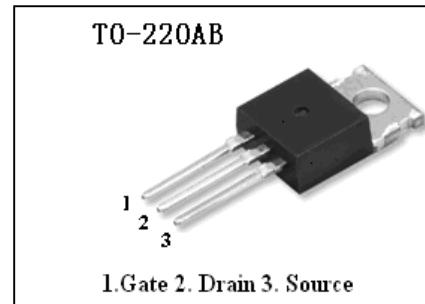
## Applications:

UPS,Inverter,Lighting.

**Absolute** ( $T_c = 25^\circ\text{C}$  unless otherwise specified):

Symbol	Parameter	Rating	Units
$V_{DSS}$	Drain-to-Source Voltage	40	V
$I_D$	Continuous Drain Current	150	A
	Continuous Drain Current $T_c = 100^\circ\text{C}$	90	A
$I_{DM}^{a1}$	Pulsed Drain Current	600	A
$V_{GS}^{a2}$	Gate-to-Source Voltage	$\pm 30$	V
$E_{AS}^{a3}$	Single Pulse Avalanche Energy	100	mJ
$dv/dt^{a3}$	Peak Diode Recovery $dv/dt$	15	V/ns
$P_D$	Power Dissipation	150	W
	Derating Factor above $25^\circ\text{C}$	1.2	W/ $^\circ\text{C}$
$T_J, T_{stg}$	Operating Junction and Storage Temperature Range	-55 to 150	$^\circ\text{C}$
$T_L$	MaximumTemperature for Soldering	300	$^\circ\text{C}$

$V_{DSS}$	40	V
$I_D$	150	A
$P_D (T_c=25^\circ\text{C})$	100	W
$R_{DS(ON)Typ}$	4	$\text{m}\Omega$



Symbol	Parameter	Typ.	Units
$R_{\theta JC}$	Junction-to-Case	0.83	$^\circ\text{C}/\text{W}$
$R_{\theta JA}$	Junction-to-Ambient	100	$^\circ\text{C}/\text{W}$

**Electrical Characteristics (Tc= 25°C unless otherwise specified):**

OFF Characteristics		Test Conditions	Rating			Units
Symbol	Parameter		Min.	Typ.	Max.	
V <sub>DSS</sub>	Drain to Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	40	--	--	V
ΔV <sub>DSS</sub> / ΔT <sub>J</sub>	vdss Temperature Coefficient	I <sub>D</sub> =250uA, Reference 25°C	--	0.035	--	V/°C
I <sub>DSS</sub>	Drain to Source Leakage Current	V <sub>DS</sub> = 40V, V <sub>GS</sub> = 0V, T <sub>a</sub> = 25°C	--	--	1	μA
		V <sub>DS</sub> = 32V, V <sub>GS</sub> = 0V, T <sub>a</sub> = 150°C	--	--	500	
I <sub>GSS(F)</sub>	Gate to Source Forward Leakage	V <sub>GS</sub> = +30V	--	--	100	nA
I <sub>GSS(R)</sub>	Gate to Source Reverse Leakage	V <sub>GS</sub> = -30V	--	--	-100	nA

ON Characteristics		Test Conditions	Rating			Units
Symbol	Parameter		Min.	Typ.	Max.	
R <sub>DS(ON)</sub>	Drain-to-Source On-Resistance	V <sub>GS</sub> =10V, I <sub>D</sub> =50A	--	4	4.5	mΩ
V <sub>GS(TH)</sub>	Gate Threshold Voltage	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA	2.0		3.5	V
g <sub>f</sub> s	Forward Transconductance	V <sub>DS</sub> =10V, I <sub>D</sub> = 50A	--	320	--	S
Pulse width t <sub>p</sub> ≤ 300μs, δ ≤ 2%						

Dynamic Characteristics		Test Conditions	Rating			Units
Symbol	Parameter		Min.	Typ.	Max.	
R <sub>g</sub>	Gate resistance	V <sub>GS</sub> =0V, f = 1.0MHz	--	1	--	Ω
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> = 0V V <sub>DS</sub> = 25V f = 1.0MHz	--	8900	--	pF
C <sub>oss</sub>	Output Capacitance		--	550	--	
C <sub>rss</sub>	Reverse Transfer Capacitance		--	480	--	

Resistive Switching Characteristics		Test Conditions	Rating			Units
Symbol	Parameter		Min.	Typ.	Max.	
t <sub>d(ON)</sub>	Turn-on Delay Time	I <sub>D</sub> = 150A V <sub>DD</sub> = 30V V <sub>GS</sub> = 10V R <sub>G</sub> = 10Ω	--	48	--	ns
tr	Rise Time		--	88	--	
t <sub>d(OFF)</sub>	Turn-Off Delay Time		--	170	--	
t <sub>f</sub>	Fall Time		--	62	--	
Q <sub>g</sub>	Total Gate Charge	I <sub>D</sub> = 20A V <sub>DD</sub> = 32V V <sub>GS</sub> = 10V	--	160	--	nC
Q <sub>gs</sub>	Gate to Source Charge		--	42	--	
Q <sub>gd</sub>	Gate to Drain ("Miller") Charge		--	33	--	

**Source-Drain Diode Characteristics**

Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
I <sub>S</sub>	Continuous Source Current (Body Diode)		--	--	150	A
I <sub>SM</sub>	Maximum Pulsed Current (Body Diode)		--	--	600	A
V <sub>SD</sub>	Diode Forward Voltage	V <sub>GS</sub> =0V, I <sub>S</sub> =50A,	--	--	0.96	V
t <sub>rr</sub>	Reverse Recovery Time	I <sub>S</sub> =20A, T <sub>j</sub> = 25°C	--	84	--	ns
Q <sub>rr</sub>	Reverse Recovery Charge	dI <sub>F</sub> /dt=100A/us, V <sub>GS</sub> =0V	--	75	--	nC

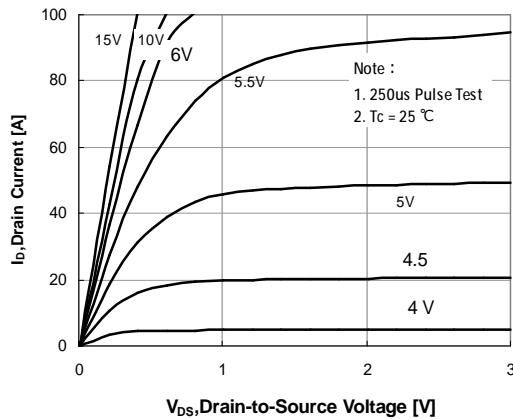
Pulse width tp≤300μs, δ ≤2%

<sup>a1</sup>: Repetitive rating; pulse width limited by maximum junction temperature<sup>a2</sup>: L=0.1mH, I<sub>D</sub>=80A, R<sub>g</sub>=25 Ω, V<sub>DD</sub>=50V ,Start T<sub>J</sub>=25°C<sup>a3</sup>: I<sub>SD</sub>=5A,di/dt≤200A/us,V<sub>DD</sub>≤BV<sub>DS</sub>, Start T<sub>J</sub>=25°C

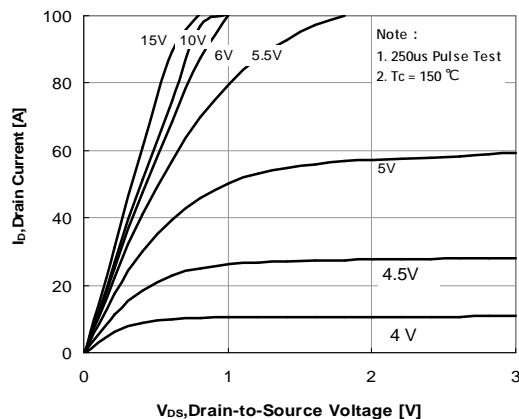


## Characteristics Curve:

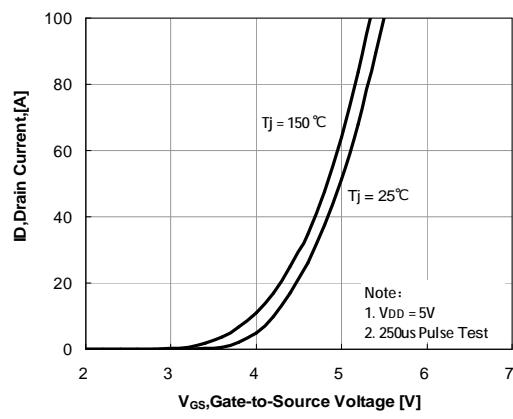
Typical Output Characteristics



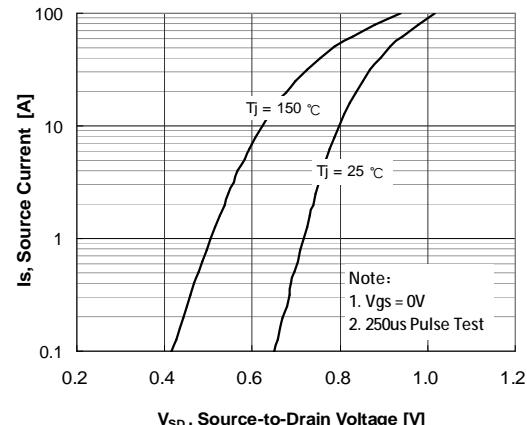
High temperature Output Characteristics



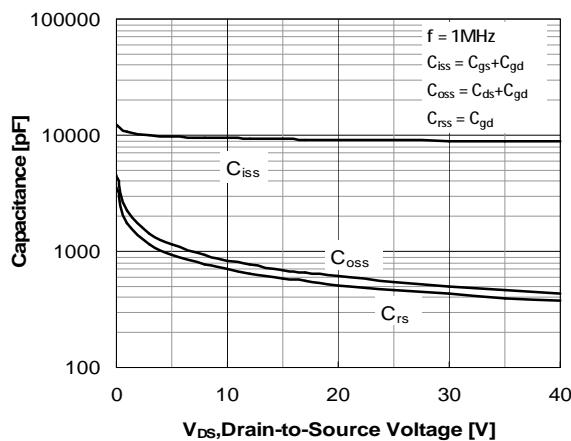
Typical Transfer Characteristics



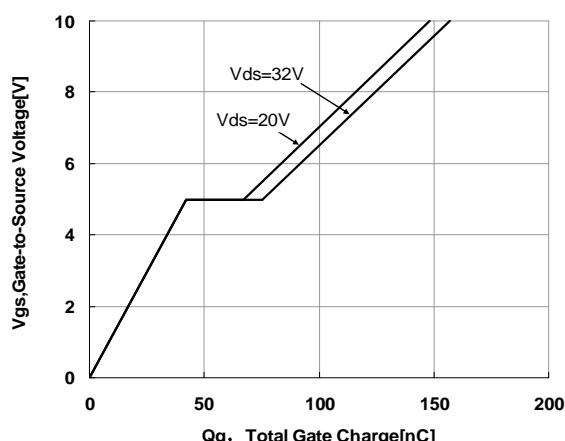
Source -to-Drain Diode Forward Voltage



Capacitance VS Drain-to-Source Voltage

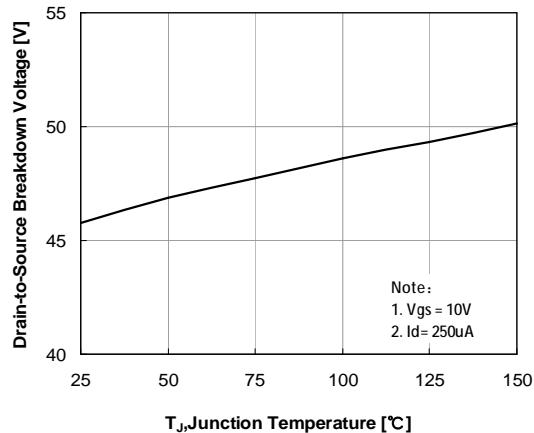


Gate to Source Voltage VS Total Gate Charge

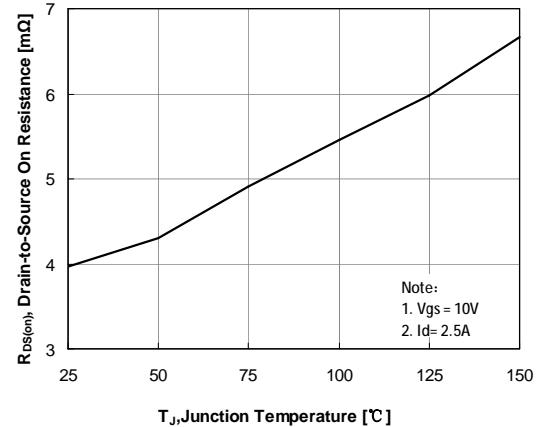




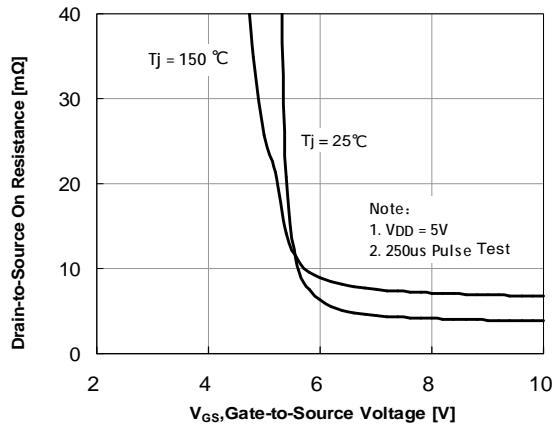
Drain-to-Source breakdown VS Case Temperature



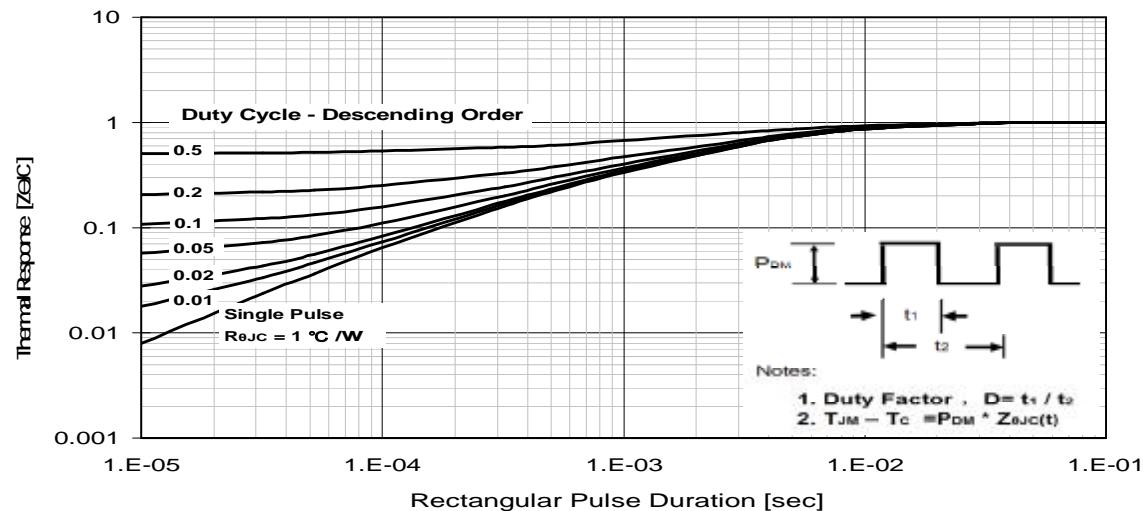
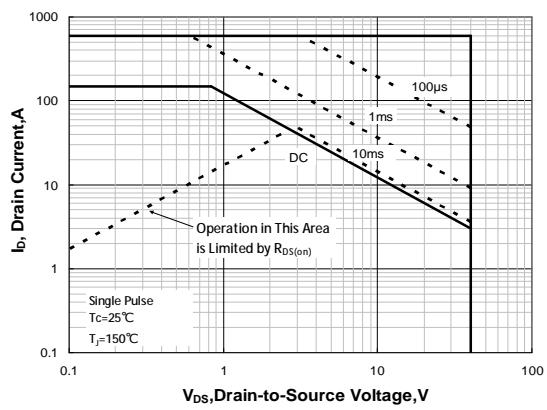
Drain-to-Source On-Resistance VS Case Temperature



Drain-to-Source On-Resistance VS Gate to Source Voltage

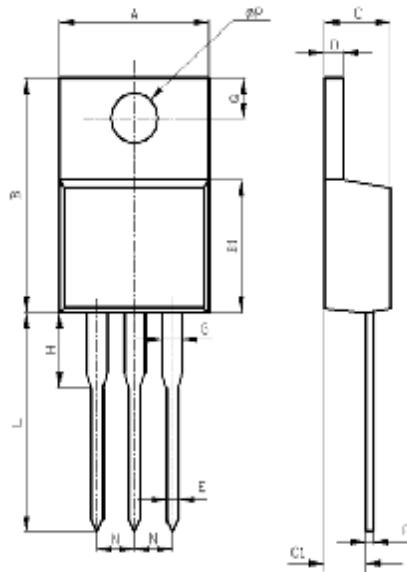


Maximum Forward Bias Safe Operating Area



Maximum Effective Thermal Impedance , Junction to Case

## Package Information



Items	Values(mm)	
	MIN	MAX
A	10.00	10.60
B	15.0	16.0
B1	8.90	9.50
C	4.30	4.80
C1	2.30	3.10
D	1.20	1.40
E	0.70	0.90
F	0.30	0.60
G	1.17	1.37
H	3.30	3.80
L	6.40	7.50
	6.70	7.90
	7.20	8.00
	7.50	8.60
	12.7	14.7
N	2.34	2.74
Q	2.40	3.00
Φ P	3.50	3.90

TO-220AB Package

**The name and content of poisonous and harmful material in products**

Part's Name	Hazardous Substance					
	Pb	Hg	Cd	Cr(VI)	PBB	PBDE
Limit	≤0.1%	≤0.1%	≤0.01%	≤0.1%	≤0.1%	≤0.1%
Lead Frame	○	○	○	○	○	○
Molding Compound	○	○	○	○	○	○
Chip	○	○	○	○	○	○
Wire Bonding	○	○	○	○	○	○
Solder	×	○	○	○	○	○
Note	○: means the hazardous material is under the criterion of SJ/T11363-2006. ×: means the hazardous material exceeds the criterion of SJ/T11363-2006. The plumbum element of solder exist in products presently, but within the allowed range of Eurogroup's RoHS.					

**Warnings**

1. Exceeding the maximum ratings of the device in performance may cause damage to the device, even the permanent failure, which may affect the dependability of the machine. It is suggested to be used under 80 percent of the maximum ratings of the device.
2. When installing the heatsink, please pay attention to the torsional moment and the smoothness of the heatsink.
3. VDMOSFETs is the device which is sensitive to the static electricity, it is necessary to protect the device from being damaged by the static electricity when using it.
4. This publication is made by Huajing Microelectronics and subject to regular change without notice.

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